

INTERFERENCE

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L31	0	(wir\$4 and bord\$4 and edge and external and area and parallel and extract\$4 and resist\$4 and capacit\$4).CLM.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 17:05

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	7227	wir\$4 same border	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:04
L2	754	(wir\$4 same border same distance) and parallel	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:04
L3	179	(wir\$4 same border same distance) and parallel and block and connect\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:04
L4	159	(wir\$4 same border same distance) and parallel and block and connect\$4 and edge	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:04
L5	7	(wir\$4 same border same distance) and "716"/\$.ccis.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:04
L6	3190	(wir\$4 same border same edge)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:04
L7	1783	(wir\$4 same border same edge) and parallel	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:04
L8	434	(wir\$4 same border same edge same distance) and parallel	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:04

L9	1	(wir\$4 same border same edge same distance) and parallel and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:04
L10	122	(wir\$4 same border same edge same distance) and parallel and block	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:04
L11	77099	(wir\$4 same (border near\$4 edge) same distance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:04
L12	193	(wir\$4 same (border near\$4 edge) same distance) and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:04
L13	160	(wir\$4 same (border near\$4 edge) same distance) and block and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:04
L14	7	(wir\$4 same border same distance) and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:04
L15	159	(wir\$4 same border same distance) and parallel and block and connect\$4 and edge and parallel	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:04
L16	0	(wir\$4 same border same edge same distance) and parallel and connect\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:04

L17	365	(wir\$4 same border same edge same distance) and parallel and connect\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:04
L18	5	(wir\$4 same border same edge) and parallel and connect\$4 and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:04
L19	1542	(wir\$4 same border same edge) and parallel and connect\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:04
L20	805	(wir\$4 same border same edge same parallel) and connect\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:04
L21	39	(wir\$4 same border) and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:04
L22	22	(bord\$4 near4 edge) and wir\$4 and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:04
L23	0	(block same border same edge) and wir\$4 and extrnal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:04
L24	253	(block same border same edge) and wir\$4 and external	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:19

L25	132	(block same border same edge) and wir\$4 and external and extract\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:56
L26	116	(block same border same edge) and wir\$4 and external and extract\$4 and delay	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:57
L27	81	(wir\$4 same bord\$4 same edge) and parallel and extract\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 16:59
L28	29	(wir\$4 same bord\$4 same edge) and parallel and extract\$4 and resist\$4 and capacit\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 17:02
L29	2995	716/5,11-13.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 17:01
L30	1	I28 and I29	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 17:01
L31	0	(wir\$4 and bord\$4 and edge and external and area and parallel and extract\$4 and resist\$4 and capacit\$4).CLM.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/12 17:05


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IEEE STD IEEE Standard

Select Article Information

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 Venier, P.; Mortara, A.; Arreguit, X.; Vittoz, E.A.;
 Solid-State Circuits, IEEE Journal of
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 Digital Object Identifier 10.1109/4.551909
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(304 KB) IEEE JNL
- ☐ **2. Proceedings of the 2003 IEEE International Symposium on Circuits and S No.03CH37430)**
 Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International
 Volume 2, 25-28 May 2003
 Digital Object Identifier 10.1109/ISCAS.2003.1205853
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